


ALAN D ALBRIGHT
UNITED STATES DISTRICT JUDGE

Term	Plaintiff's Proposed Construction	Defendants' Proposed Construction	Court's Final Construction
<p>“light load control circuit”</p> <p>U.S. Patent No. 8,400,790, Claim 1</p> <p>Proposed by Meraki</p>	Plain and ordinary meaning	“a control circuit that enables or disables synchronous rectification based on whether a pre-defined light load condition is sensed”	Plain and ordinary meaning
<p>“selectively latching [/turn] off synchronous rectification of said synchronous rectifier”</p> <p>U.S. Patent No. 8,400,790, Claim 1</p> <p>Proposed by Meraki</p>	Plain and ordinary meaning	<p>This term is indefinite.</p> <p>To the extent that it is not indefinite, it should be construed to mean “disabling the synchronous rectification mode so that said synchronous rectifier is in a nonsynchronous rectification mode under light load conditions based upon a comparison between the gate voltage of the synchronous rectifier and a predefined threshold voltage.”</p>	Not indefinite. Plain and ordinary meaning
<p>“pulse circuit”</p> <p>U.S. Patent No. 10,432,104, Claim 1, 3, 5, 8-10</p> <p>Proposed by Meraki</p>	Plain and ordinary meaning	Plain and ordinary meaning. It means “a circuit that when triggered will generate a pulse having a finite width”	Plain-and-ordinary meaning

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<p>“synchronous rectifier”</p> <p>U.S. Patent No. 10,432,104, Claim 1, 3, 5, 8-10</p> <p>Proposed by Meraki</p>	Plain and ordinary meaning	<p>“MOSFET (Metal Oxide Semiconductor Field Effect Transistor) used to rectify the signal on the secondary of the transformer in a flyback converter”</p>	Plain-and-ordinary meaning
<p>“voltage across the synchronous rectifier”</p> <p>U.S. Patent No. 10,432,104, Claim 1, 3, 11</p> <p>Proposed by Meraki</p>	Plain and ordinary meaning	<p>“a drain-source voltage VDS, i.e., the difference between the drain and source voltages of the synchronous rectifier and not the drain voltage alone”</p>	Plain and ordinary meaning
<p>“pulse maintaining [/maintains for] a [preset] time period”</p> <p>U.S. Patent No. 10,432,104, Claim 1, 3, 11</p> <p>Proposed by Meraki</p>	Plain and ordinary meaning	<p>“pulse width maintaining/maintains for a preset time period”</p>	Plain-and-ordinary meaning

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<p>“the [pulse] time period [of the pulse signal] could be adjusted by the slew rate threshold adjusting signal”</p> <p>U.S. Patent No. 10,432,104, Claim 1, 8</p> <p>Proposed by Meraki</p>	<p>Plain and ordinary meaning</p>	<p>This term is indefinite.</p> <p>To the extent that it is not indefinite, it should be construed to mean “pulse width of the pulse signal could be adjusted by the adjustable slew rate voltage threshold that is compared to the drain-source voltage of the synchronous rectifier MOSFET.”</p>	<p>Not indefinite. Plain-and-ordinary meaning.</p>
<p>“energy storage component”</p> <p>U.S. Patent No. 10,432,104, Claim 1</p> <p>Proposed by Meraki</p>	<p>Plain and ordinary meaning</p>	<p>“a transformer”</p>	<p>Plain and ordinary meaning</p>